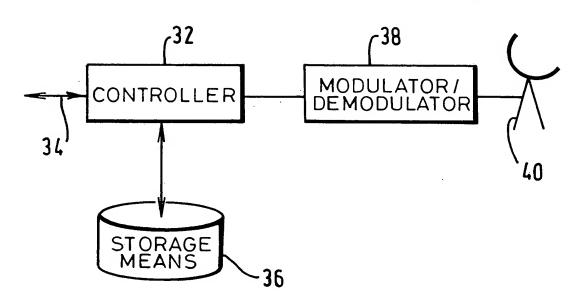
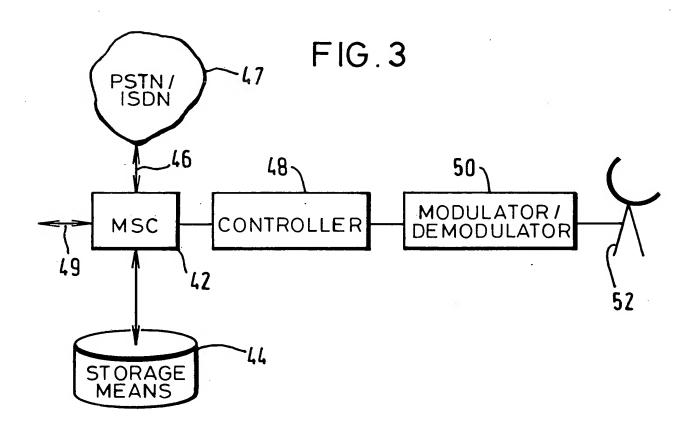
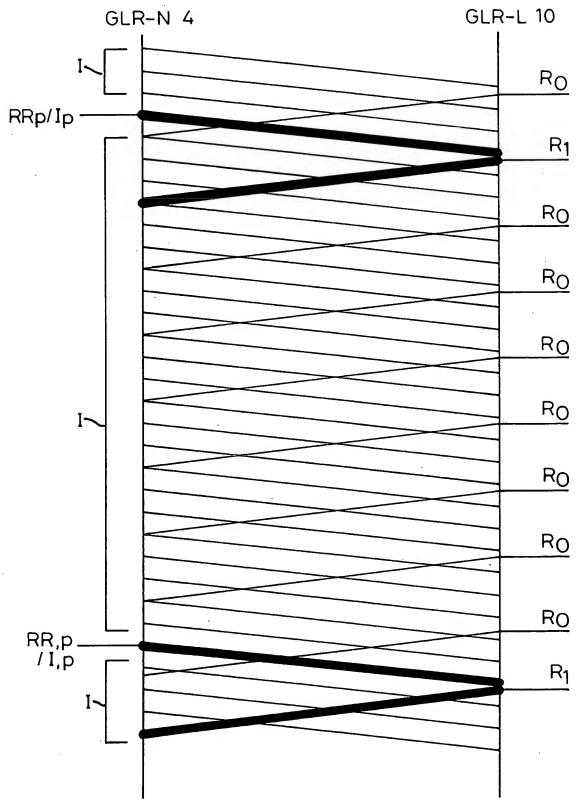


FIG.2









		1 2 3 4 5 6 7 8	SPARE IV	N(R)	SPARE V	N(R)	SPARE V	N(R)
CONTROL INFORMATION	48 BITS	5678		1000000			·	
CONTROL	16 BITS	1 2 3 4 5 6 7 8 1 2 3 4 5 6 7	10111F		L (- L	F1G. 5		

FIG.6

